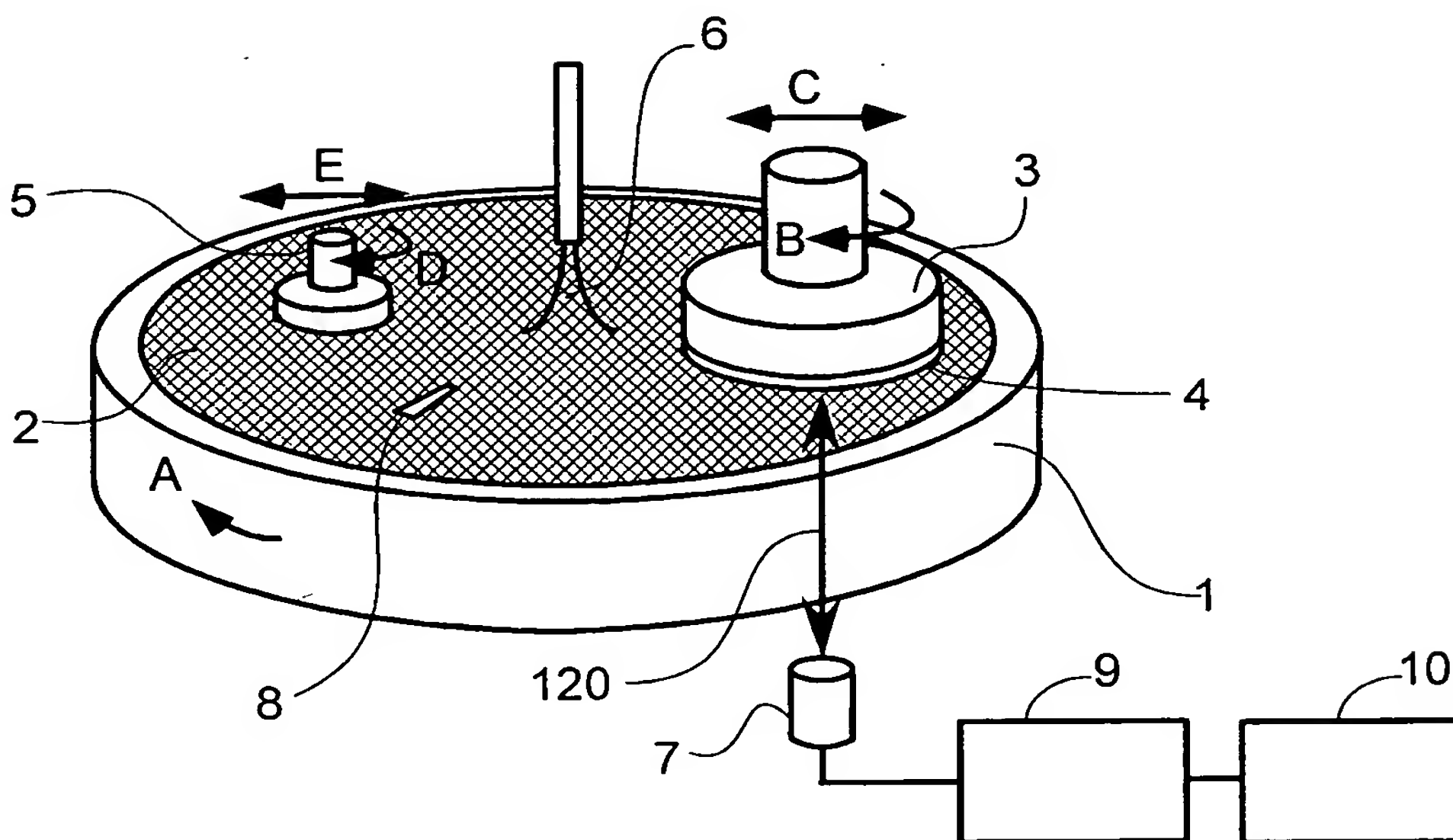


FIG.1



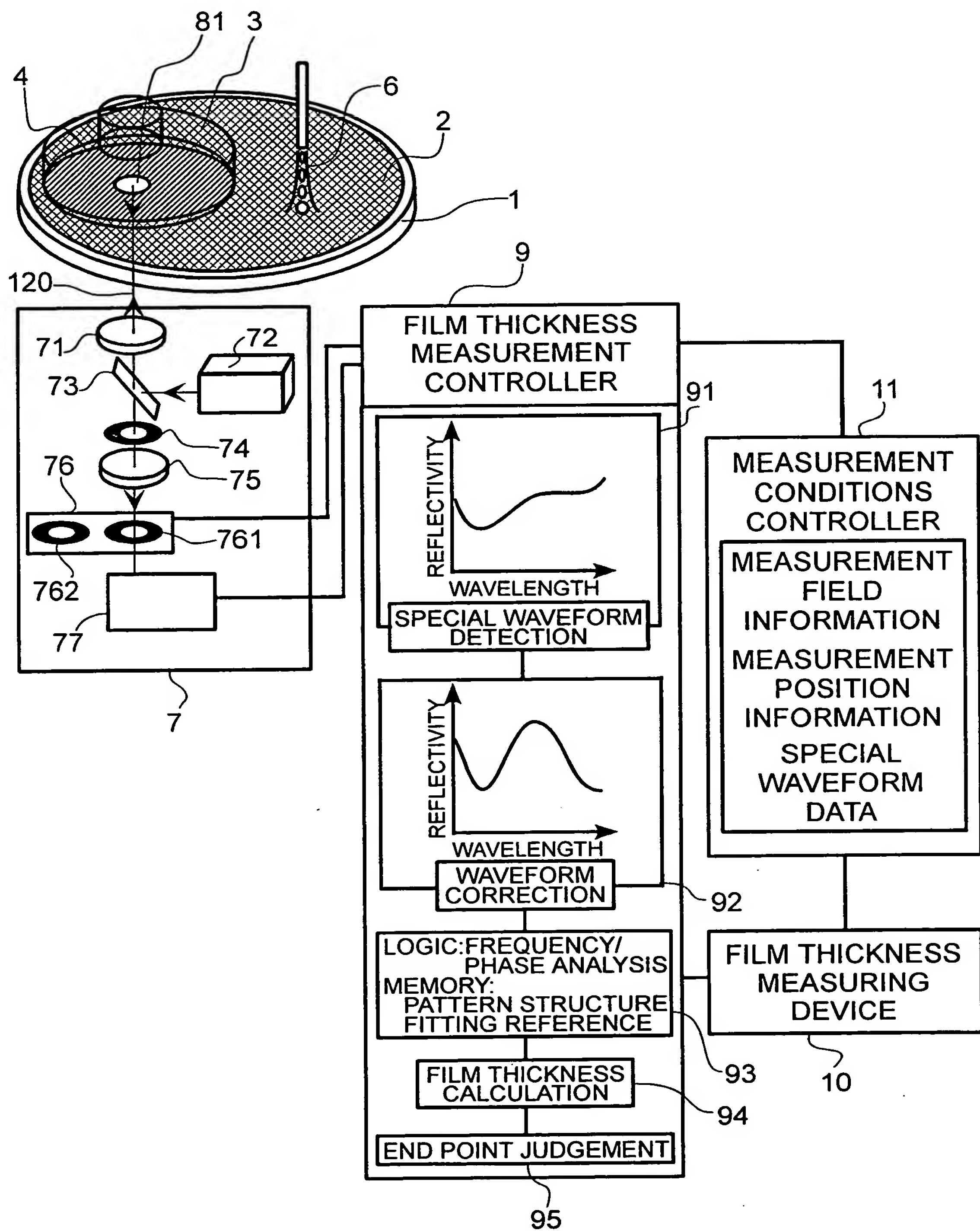
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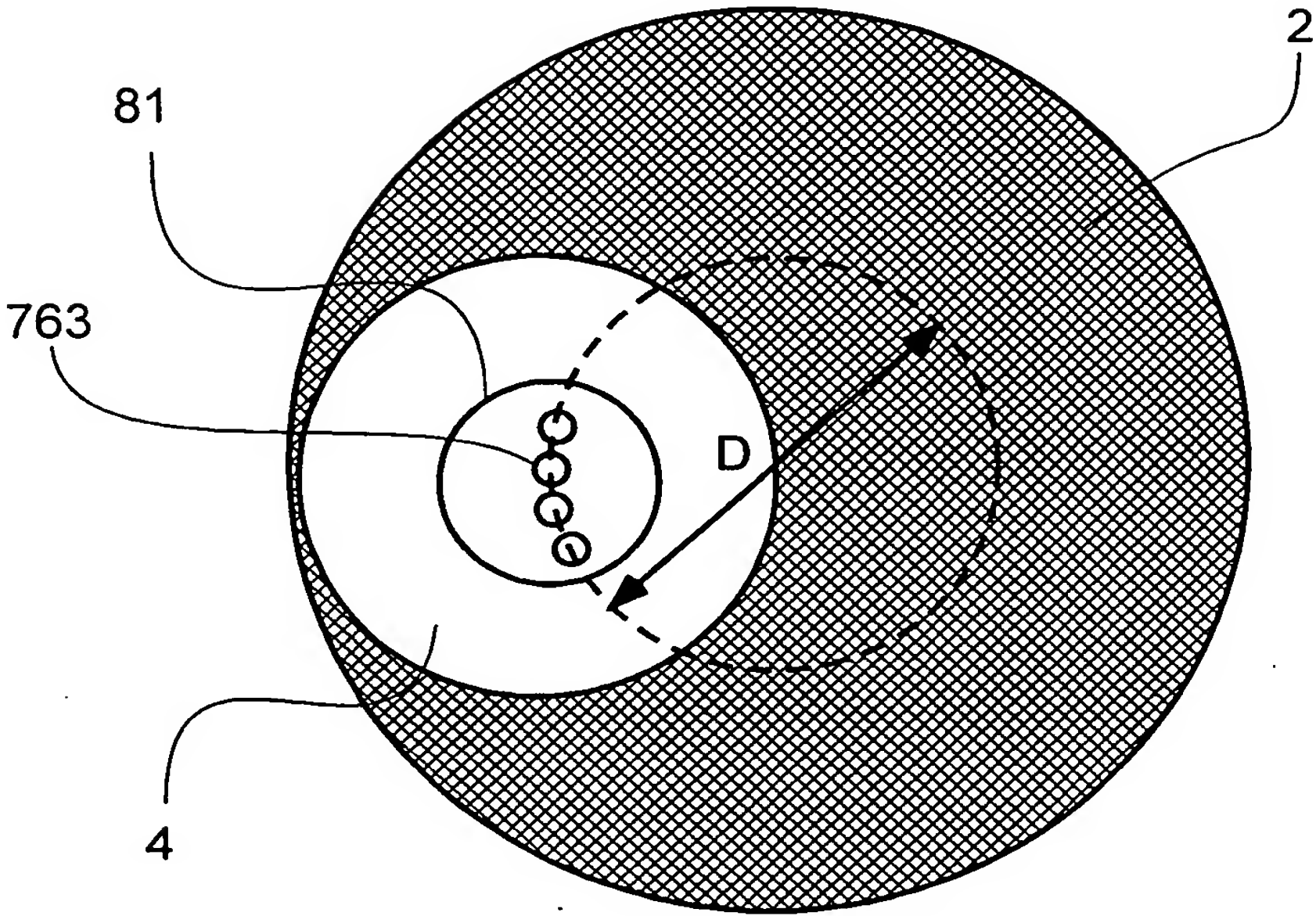
FIG. 2



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FIG.3



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FIG.4

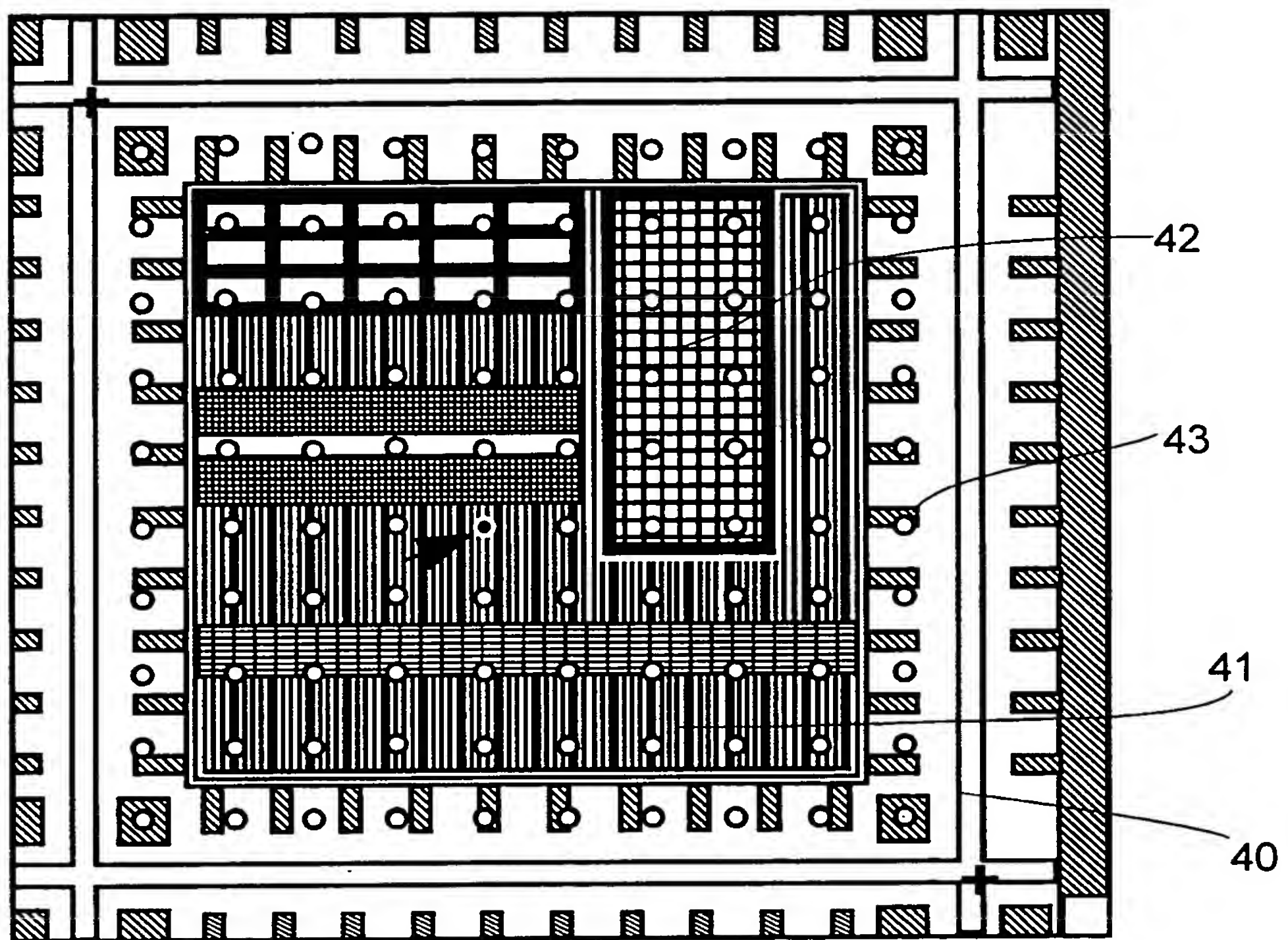


FIG.5

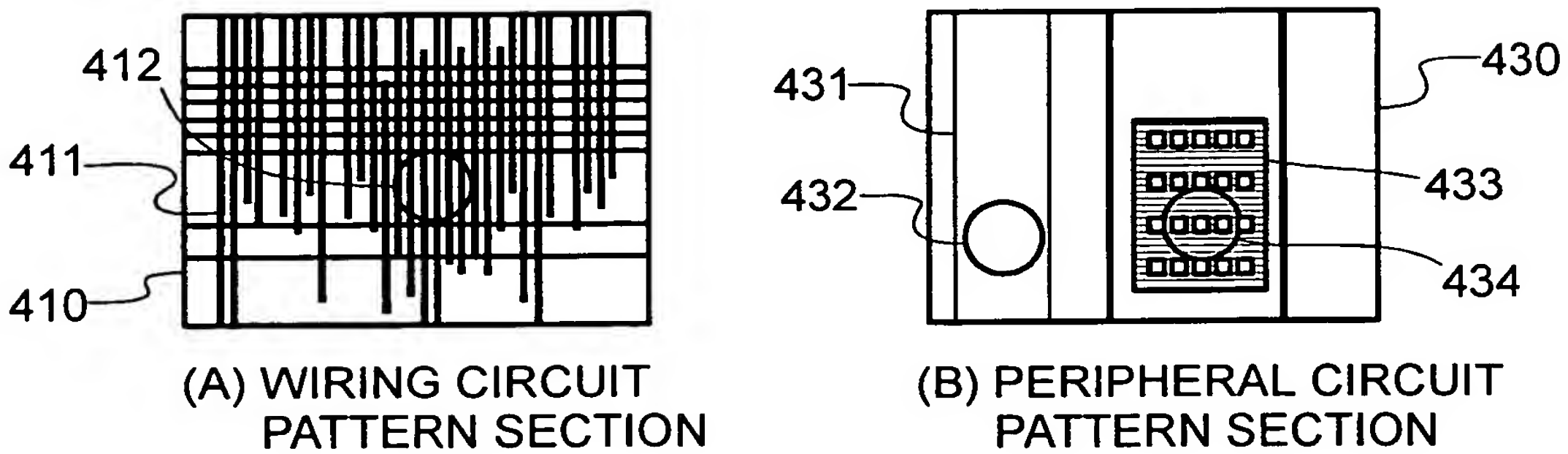


FIG.6

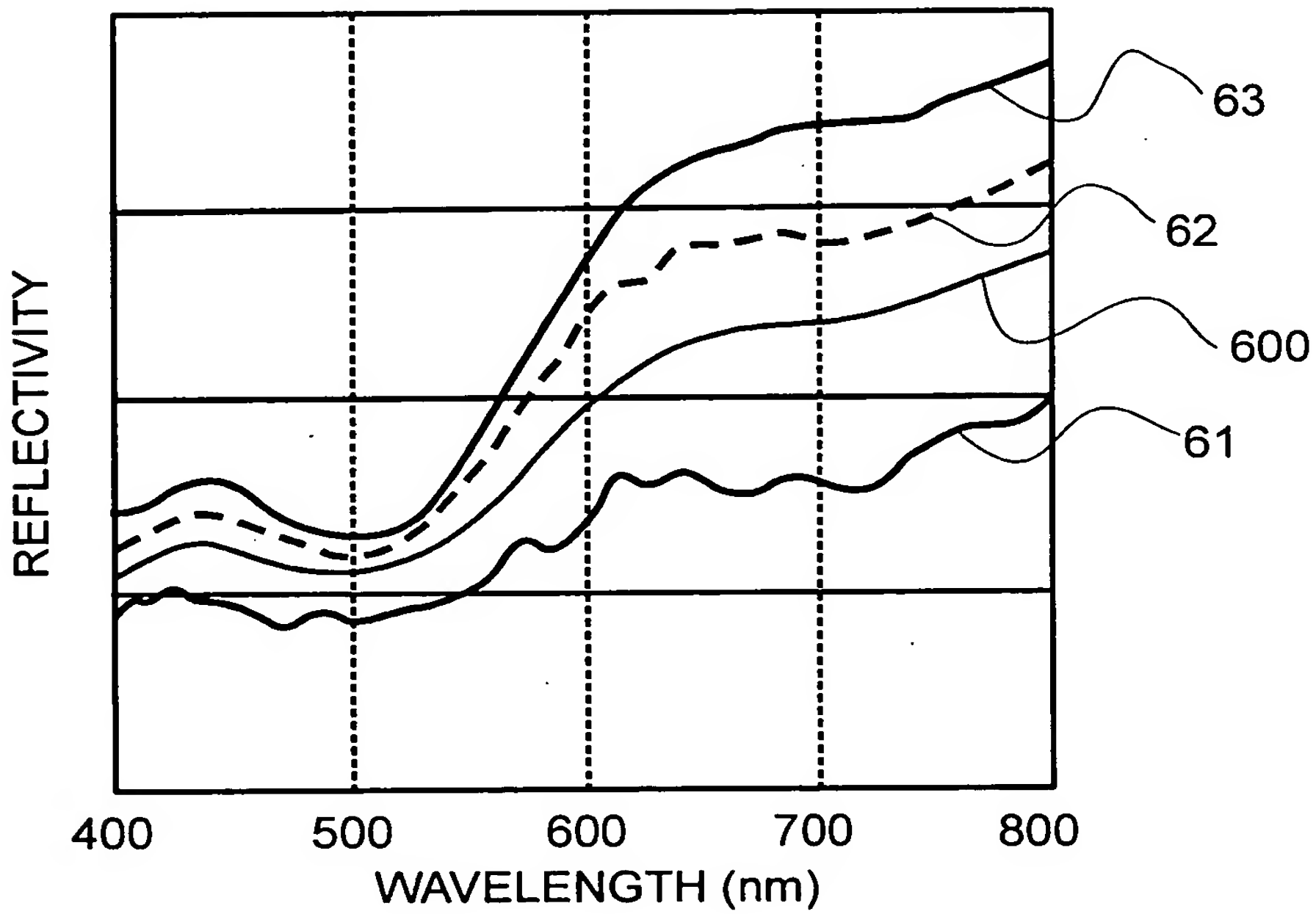
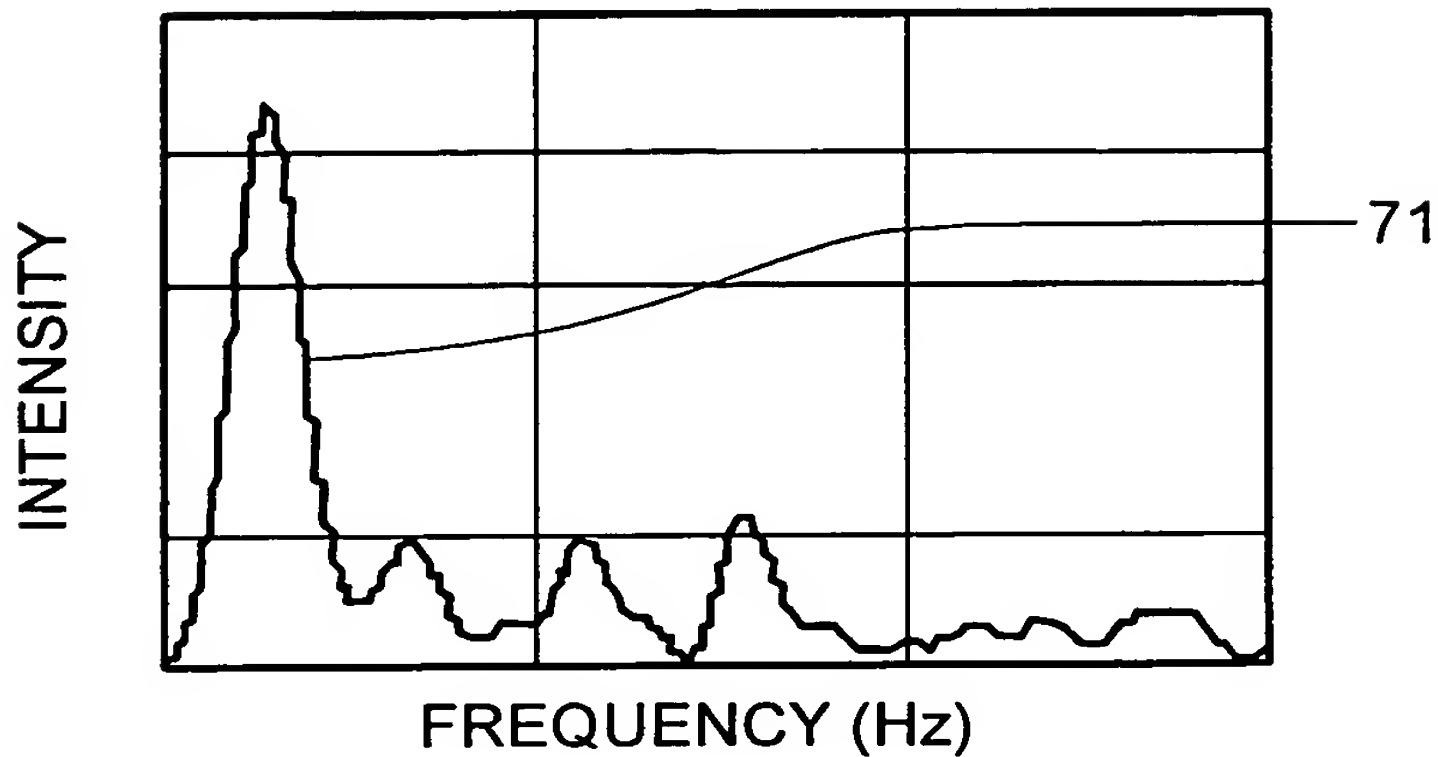
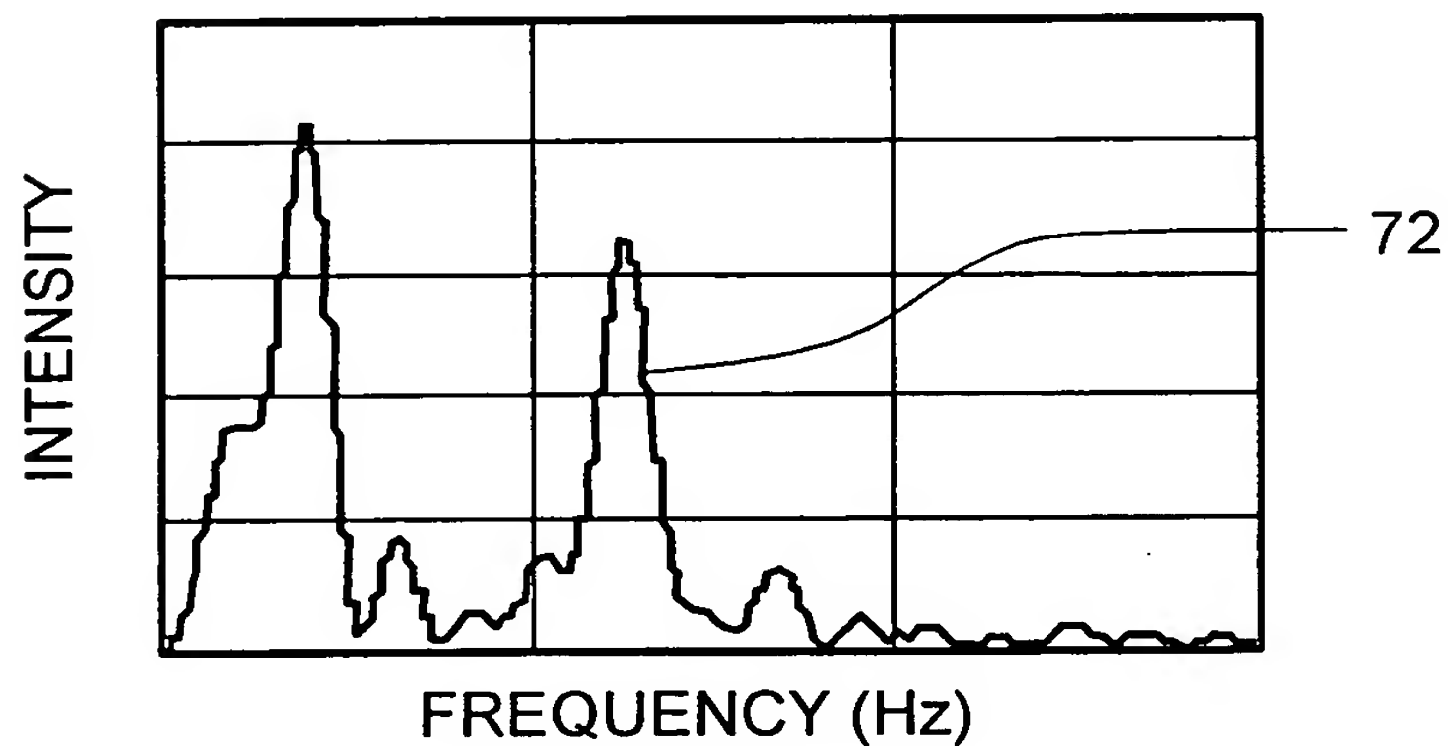


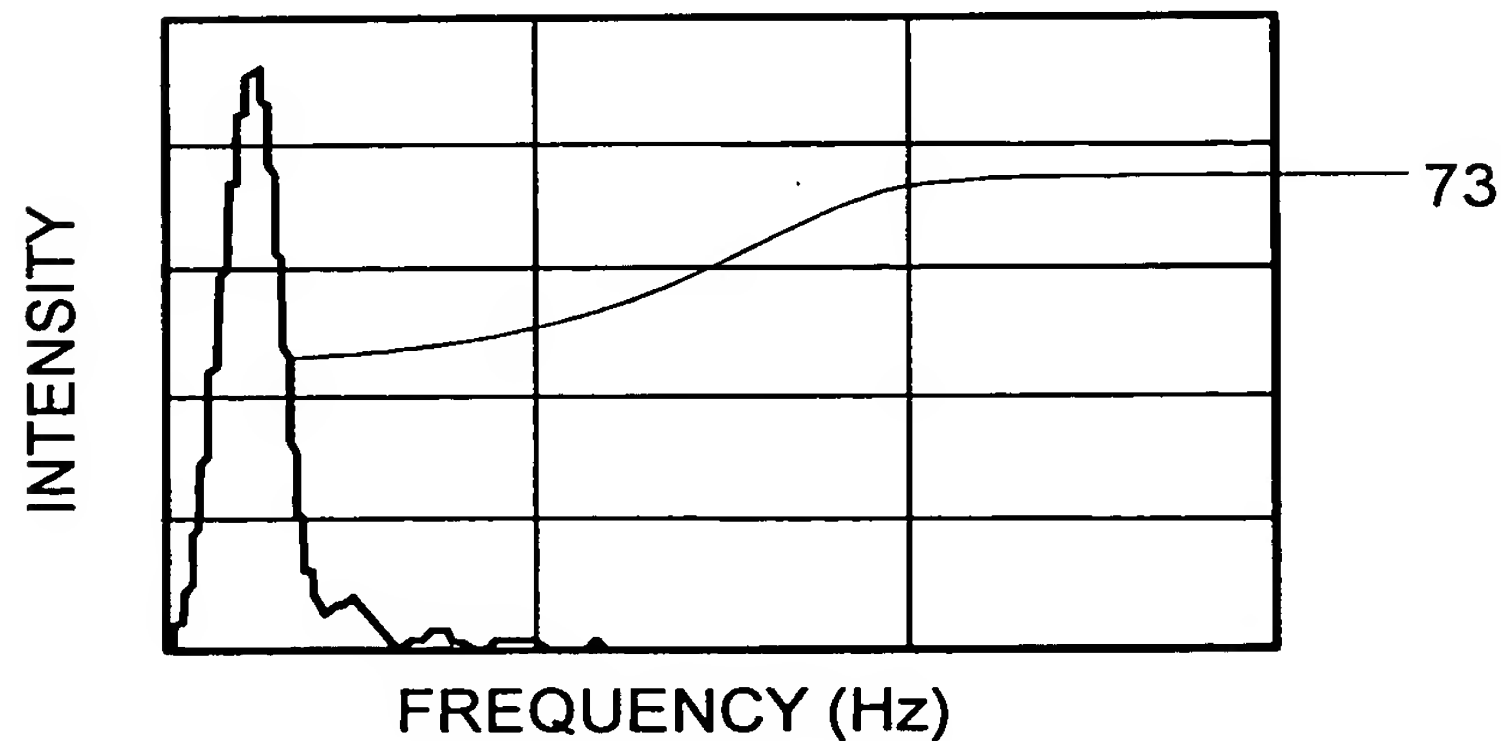
FIG.7



(a) WIRING CIRCUIT SECTION



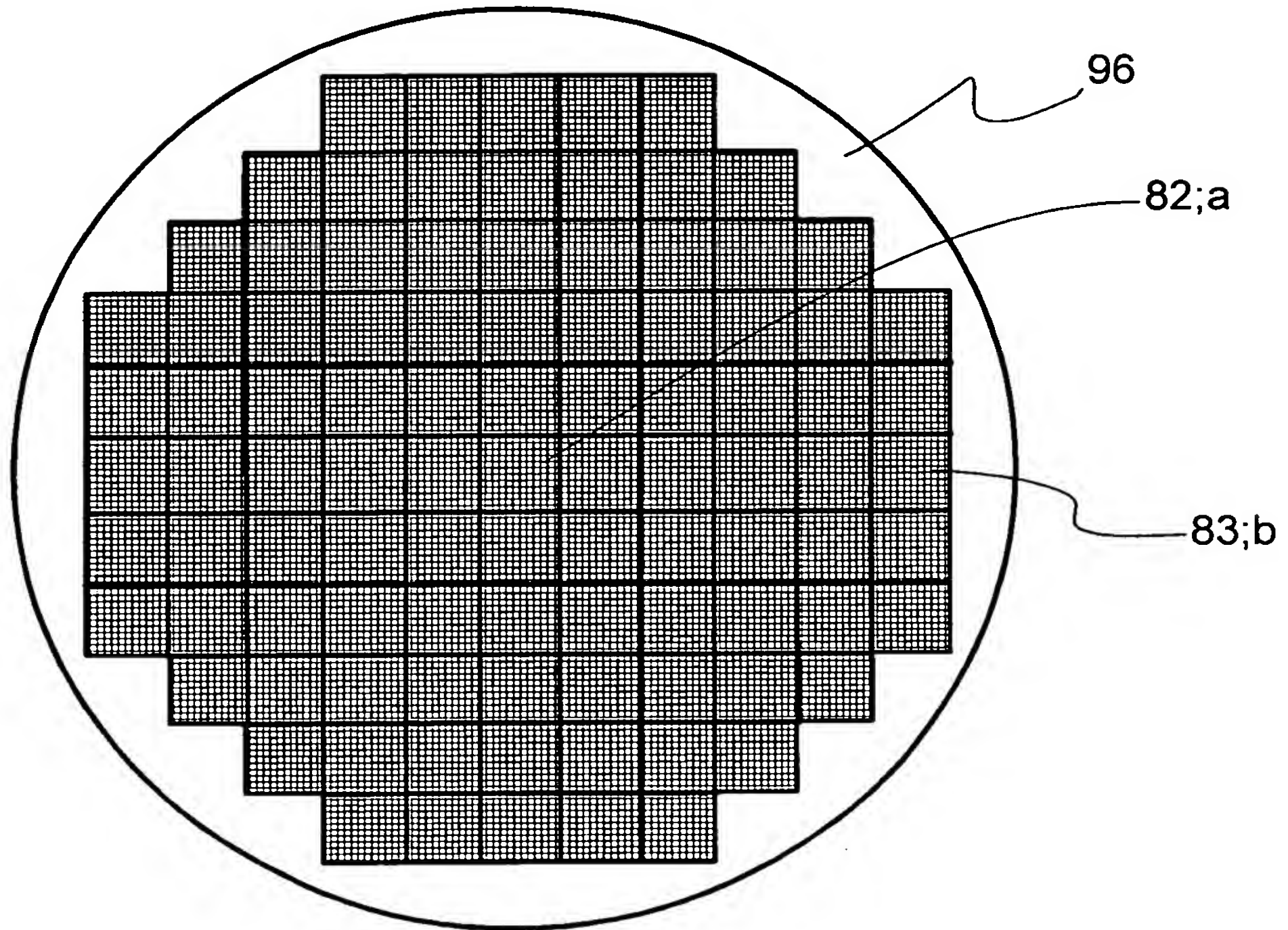
(b) MEMORY CIRCUIT SECTION



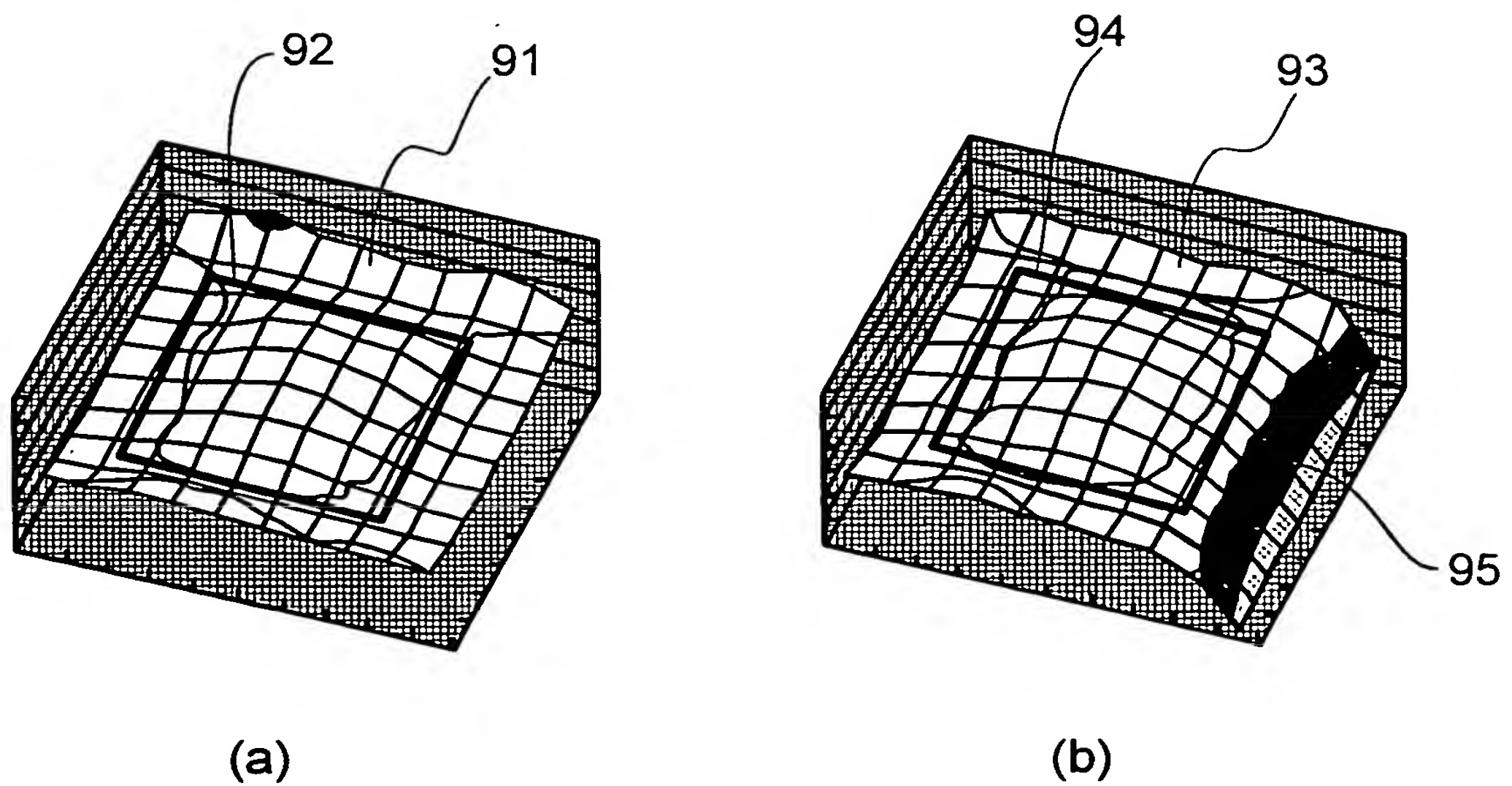
(c) PERIPHERAL CIRCUIT SECTION



**FIG.8**



**FIG.9**



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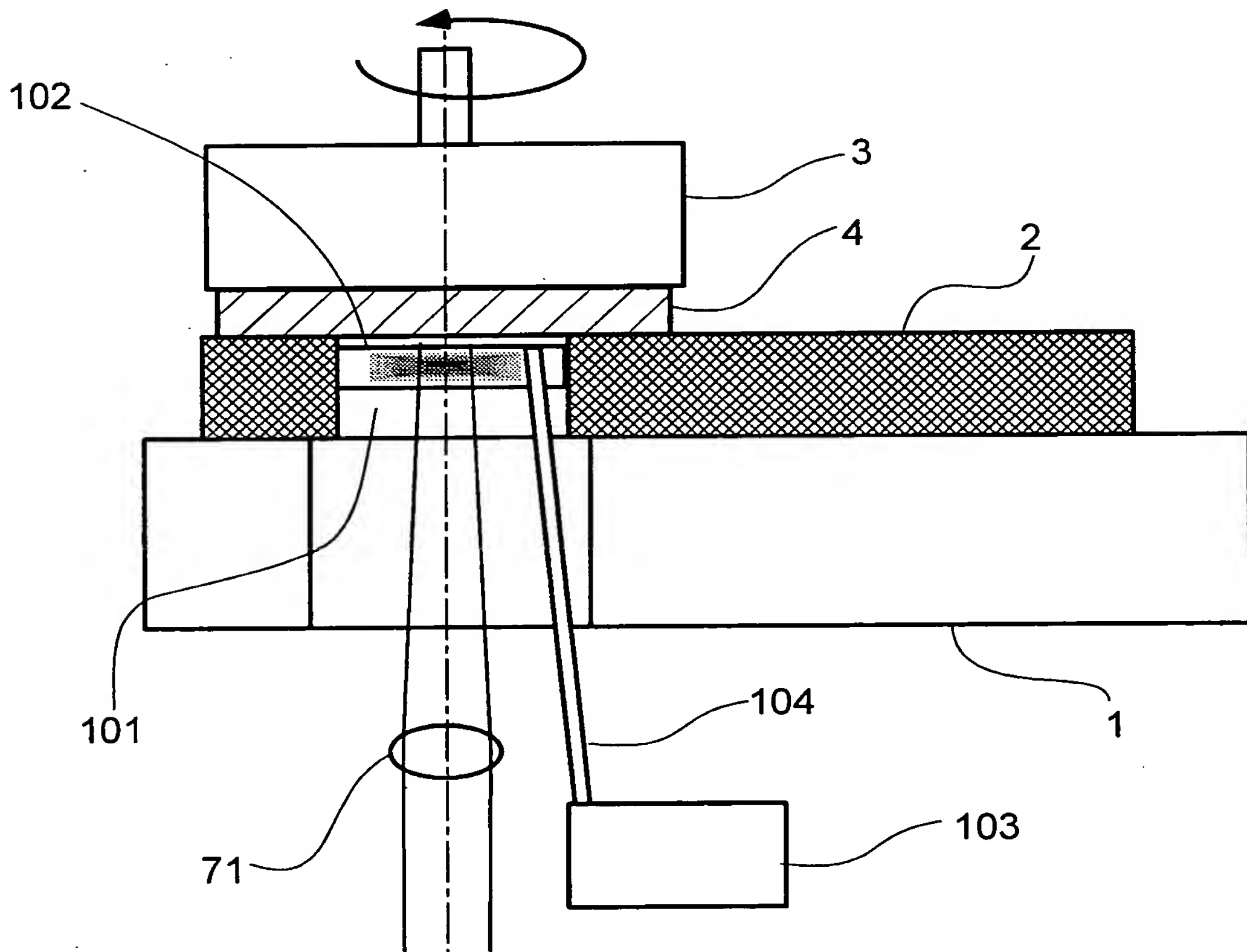
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FIG.10



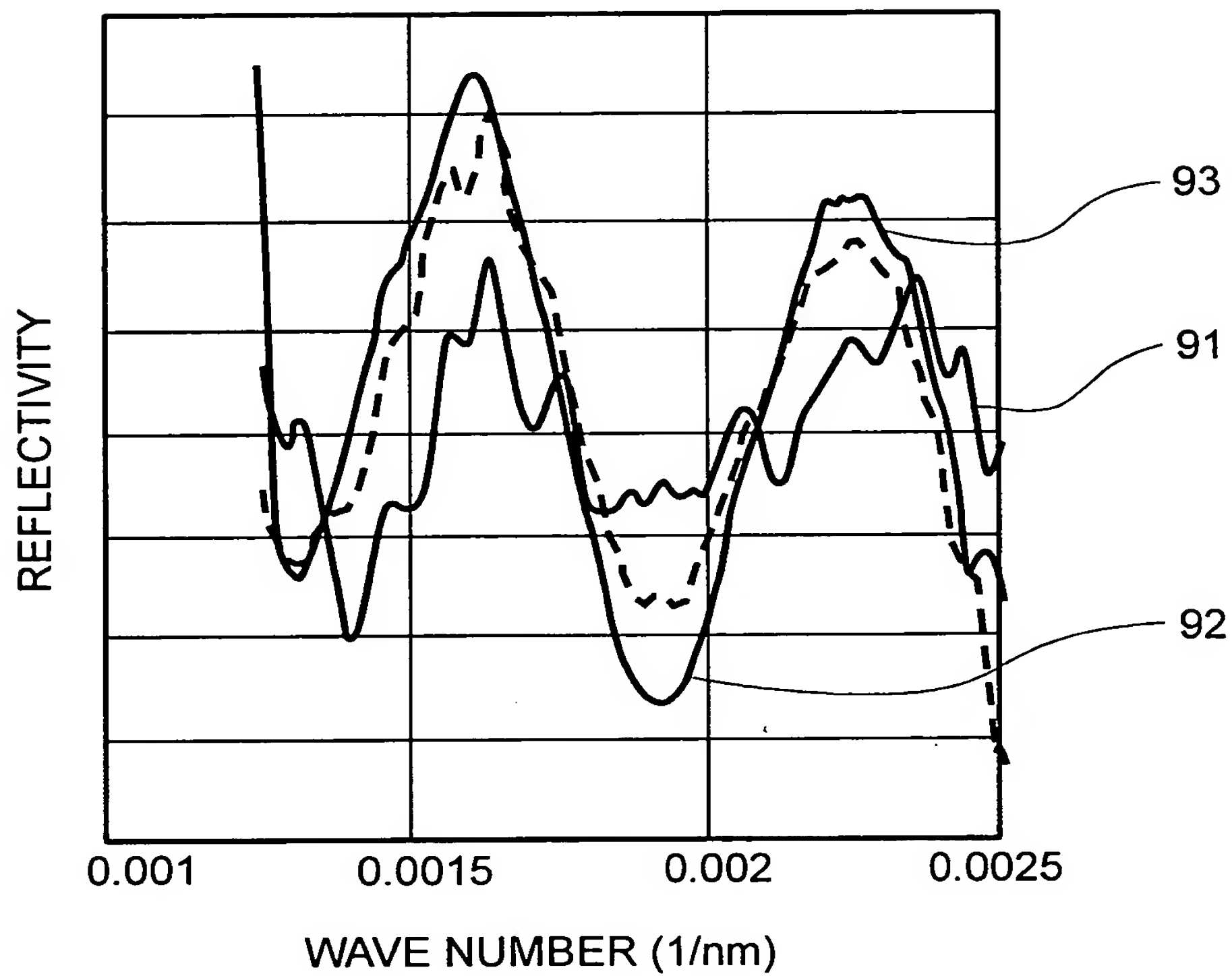


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**FIG.11**

**FIG.12**

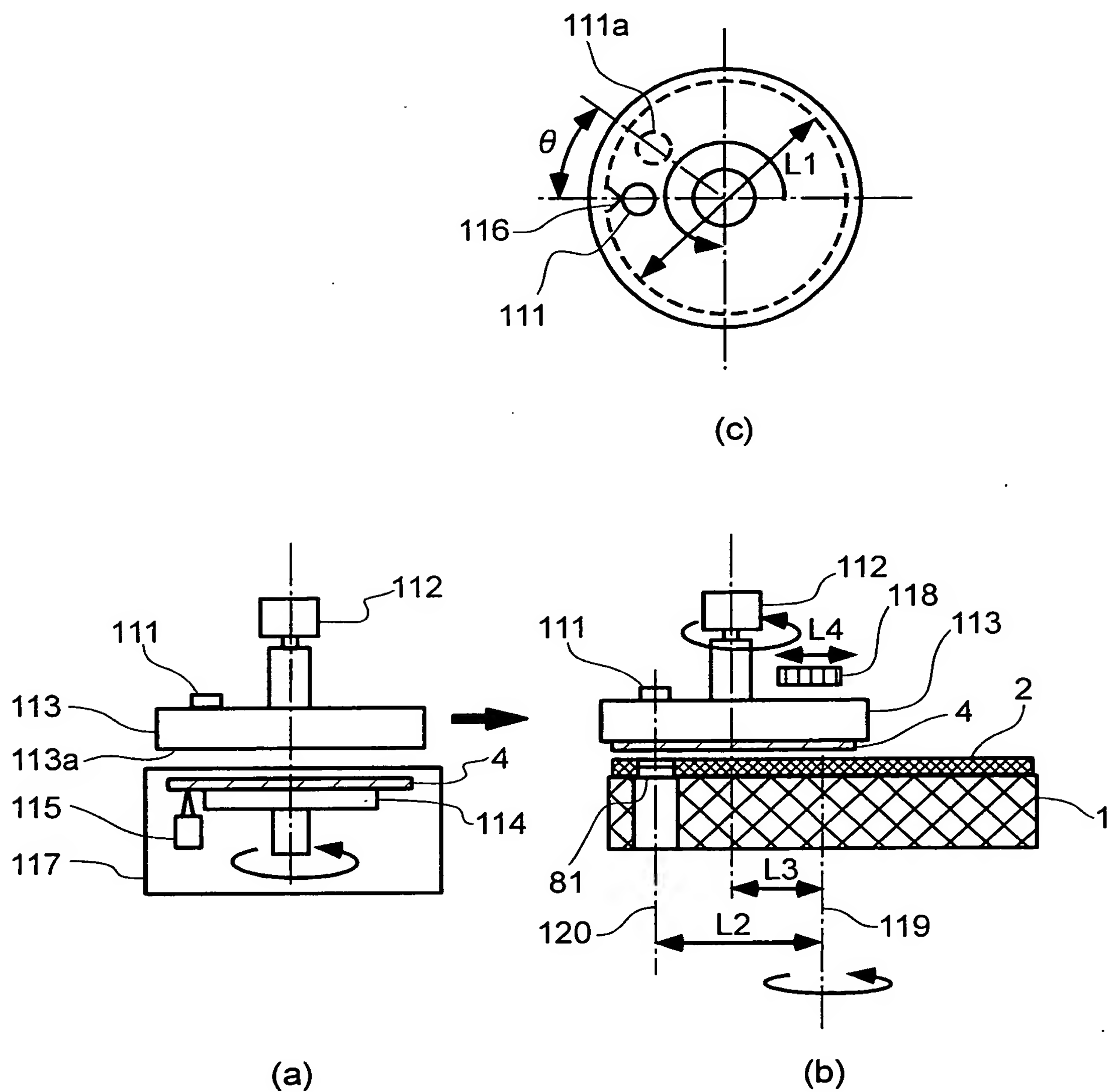
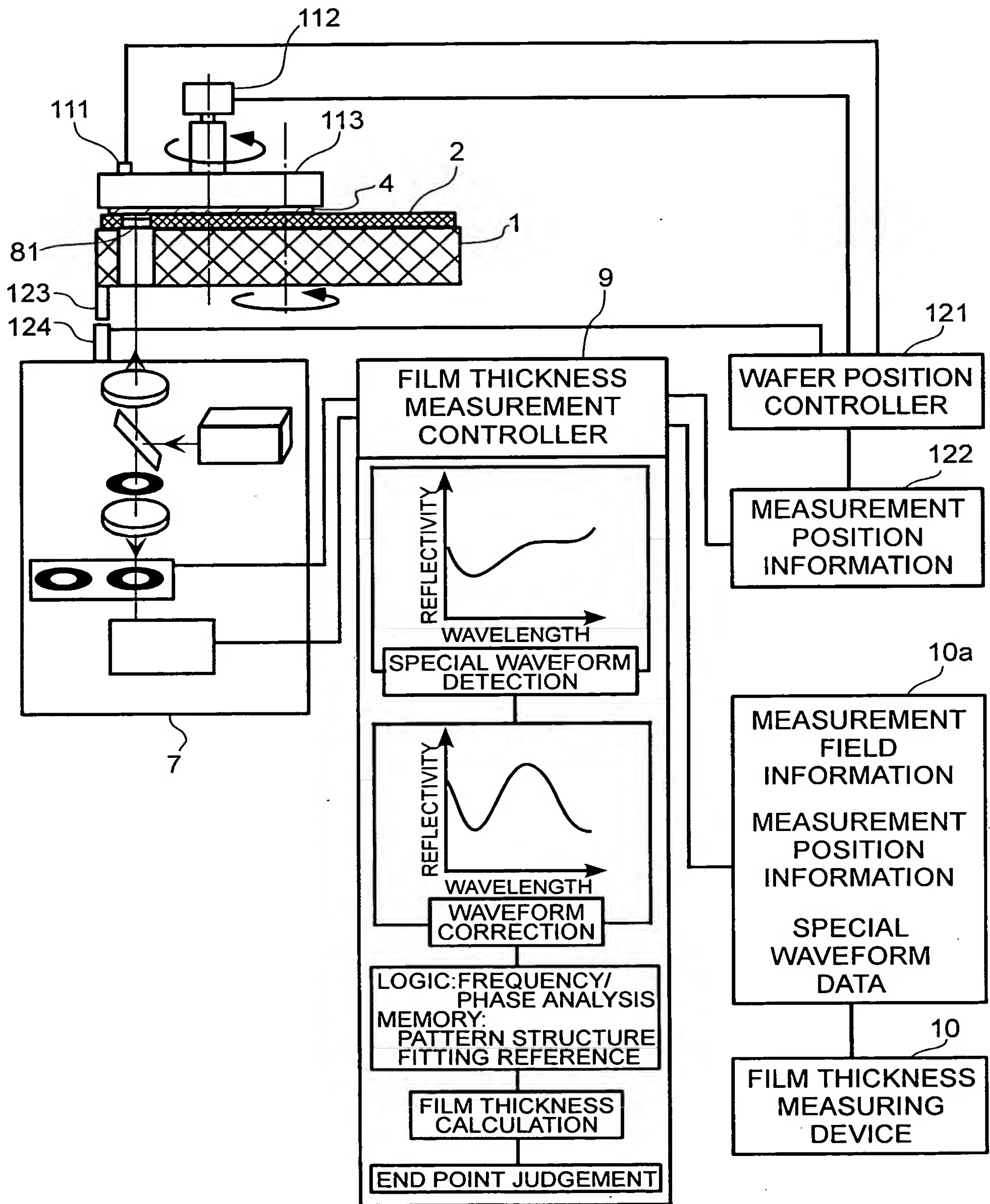


FIG. 13



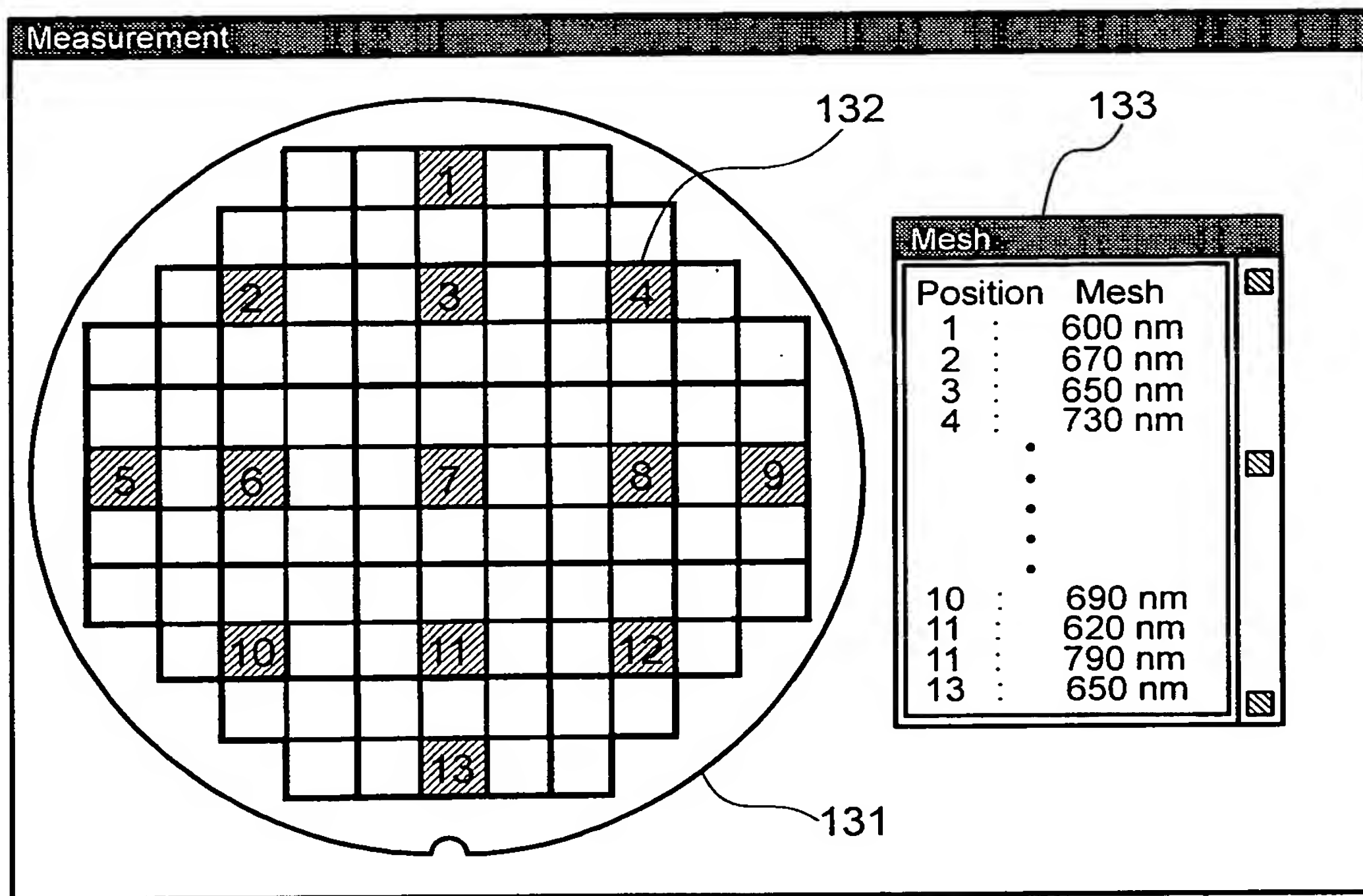
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FIG.14



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FIG.15

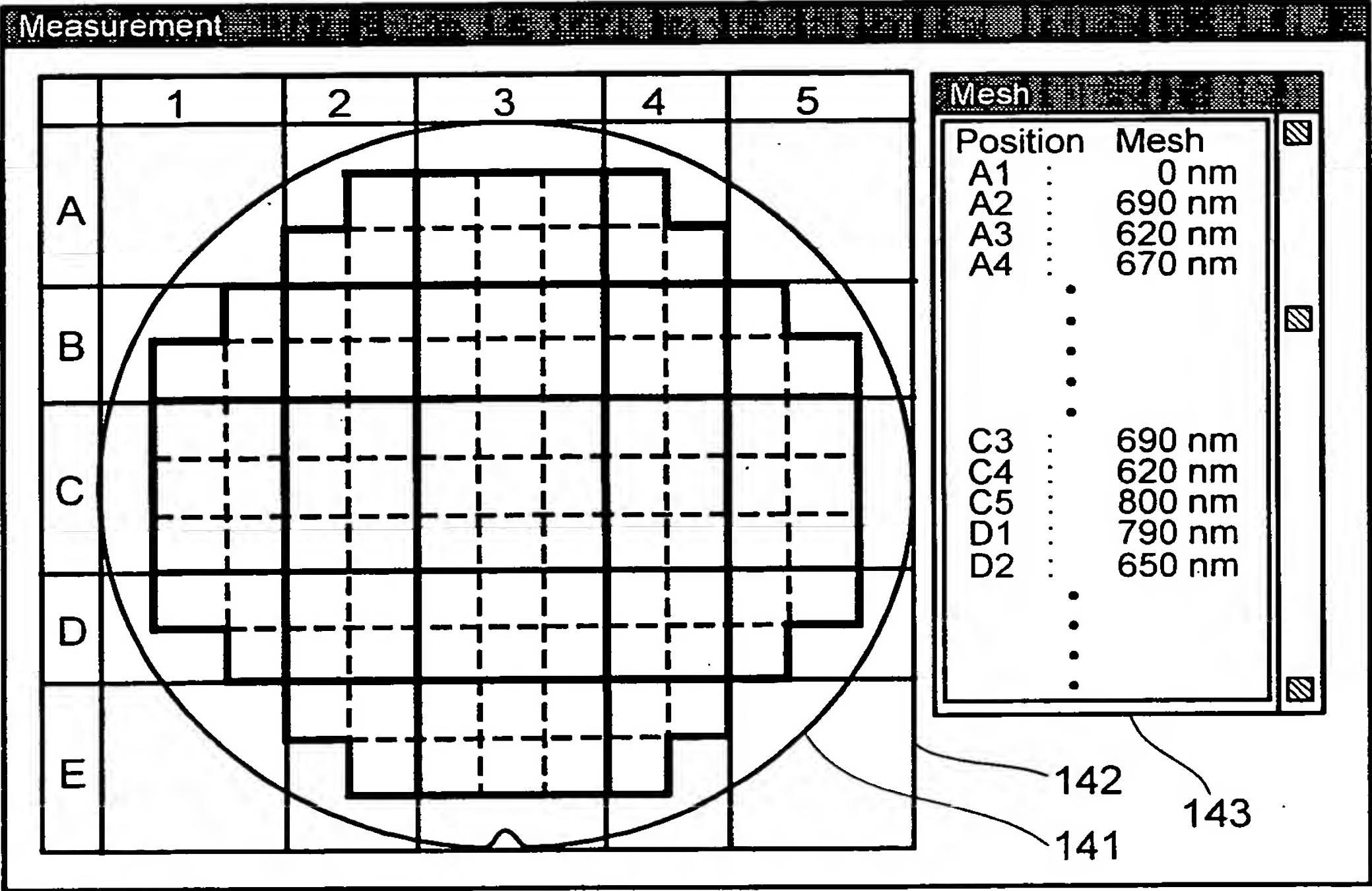
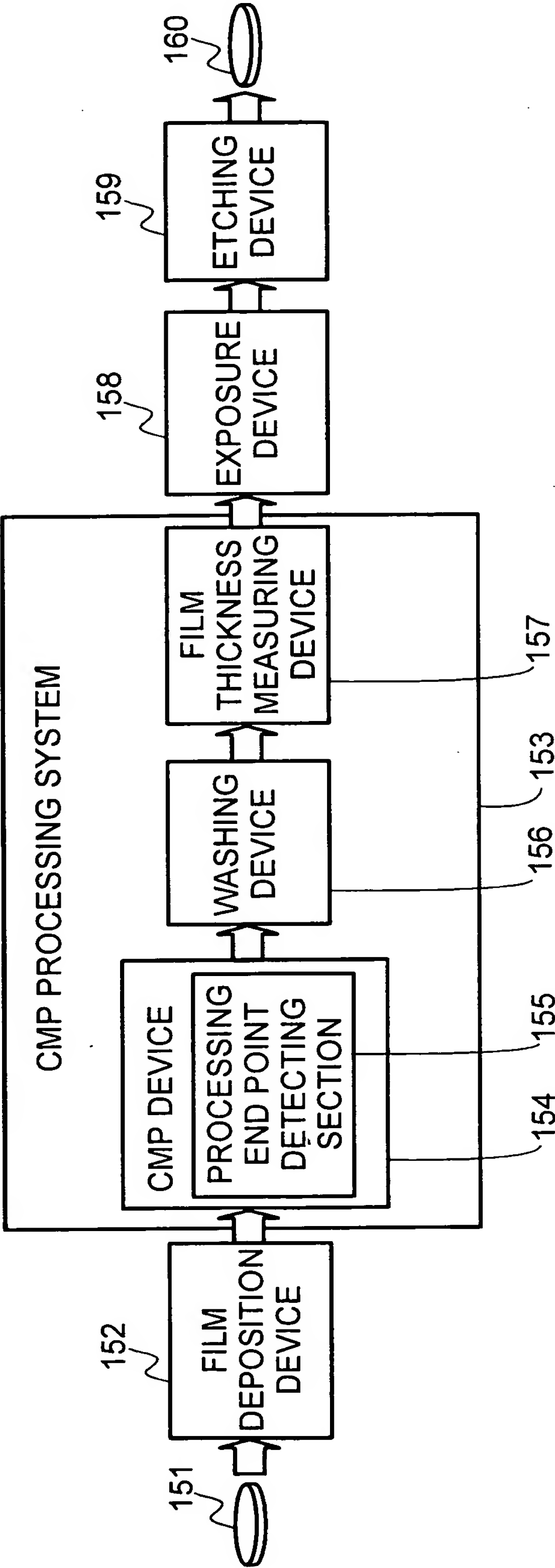


FIG.16



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FIG.17

